

# ABSTRACT OF THE DISCLOSURE

A CPU (50) generates a real recording address  $y$  from  $y = n(x-m) + m$ , where absolute time  $x$  is obtained from an ATIP  
5 decoder (48). Recording density scale factor is denoted by  $n$   
and recording start address is denoted by  $m$ . Using the real  
recording address  $y$ , an encoder (40) performs encoding and data  
recording is performed.

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